

215551US2

"RESPONSE UNDER 37 CFR 1.116-
EXPEDITED PROCEDURE EXAMINING
GROUP 2822"

AMENDMENT UNDER 37 C.F.R. §1.116 -
EXPEDITED PROCEDURE EXAMINING GROUP 2822

IN THE UNITED STATES PATENT & TRADEMARK OFFICE

IN RE APPLICATION OF

HIDEKI TAKAHASHI

SERIAL NO: 09/986,277

FILED: NOVEMBER 8, 2001

FOR: SEMICONDUCTOR DEVICE
HAVING FIRST AND SECOND
TRENCHES WITH NO CONTROL
ELECTRODE FORMED IN THE
SECOND TRENCH

:

: EXAMINER: LEWIS, M.

:

: GROUP ART UNIT: 2822

:

#1513
Waters
6/11/03
Entered
7/7/03
KOB

TECHNOLOGY CENTER 2800

JUN - 6 2003

RECEIVED

AMENDMENT UNDER 37 C.F.R. § 1.116

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

SIR:

In response to the Office Action dated March 6, 2003, please amend this application
as follows:

IN THE CLAIMS

Please amend Claims 1 and 3 as shown in clean form below.¹

1. (Twice Amended) A semiconductor device comprising:

a first semiconductor layer of a first conductivity type having first and second major
surfaces;

¹ A marked-up copy of the amended claim is attached.